AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

Serial Number: 10/797,508 Filing Date: March 10, 2004

Title: PHASE INDICATION APPARATUS

Assignee: Intel Corporation

Dkt: 884.367US3 (INTEL)

IN THE ABSTRACT OF THE DISCLOSURE

Please delete the previous Abstract and substitute the following in its place:

Abstract of the Disclosure

A variety of embodiments may include a phase lock loop, perhaps as part of an integrated circuit. In some embodiments, the phase lock loop may include a voltage-to-current circuit to influence a voltage on a capacitor, and a voltage controlled oscillator responsive to the voltage on the capacitor. In some embodiments, the phase lock loop may include a voltage controlled oscillator to generate a differential signal, and a linear voltage-to-current converter responsive to samples of the differential signal to create a control voltage for the voltage controlled oscillator.